

ELECTRONICS R&D

@ UNIPV - Incontro Studenti
29/05/2018

Confidential by MTA
Company Presentation | 2018

Product example



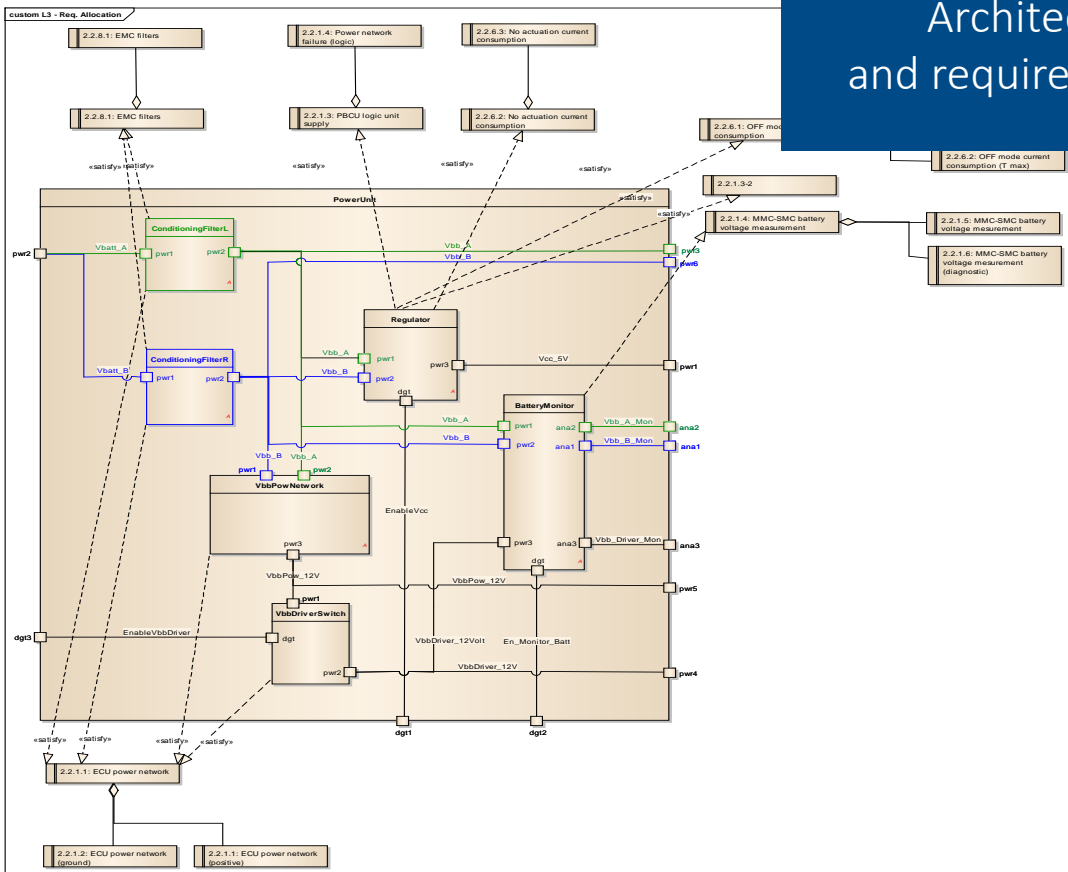
LAMBORGHINI
HURACÁN



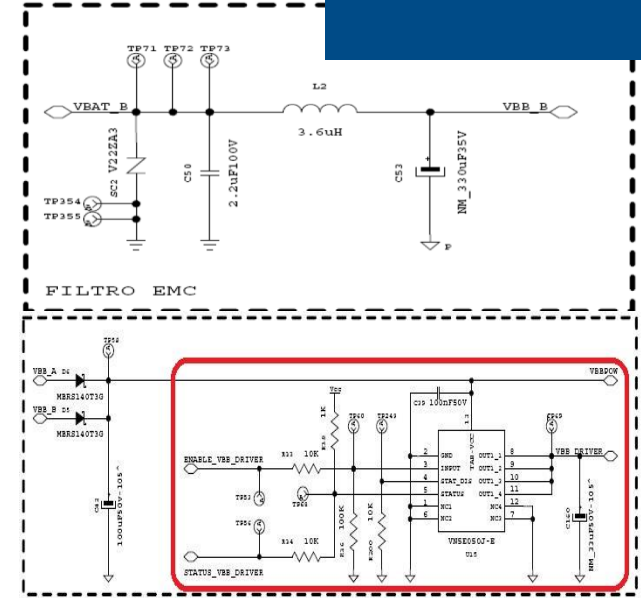
HARDWARE DESIGN

DESIGN PROCESS

Detailed design



Architectural design and requirements allocation



5.4.1 Application of requirements

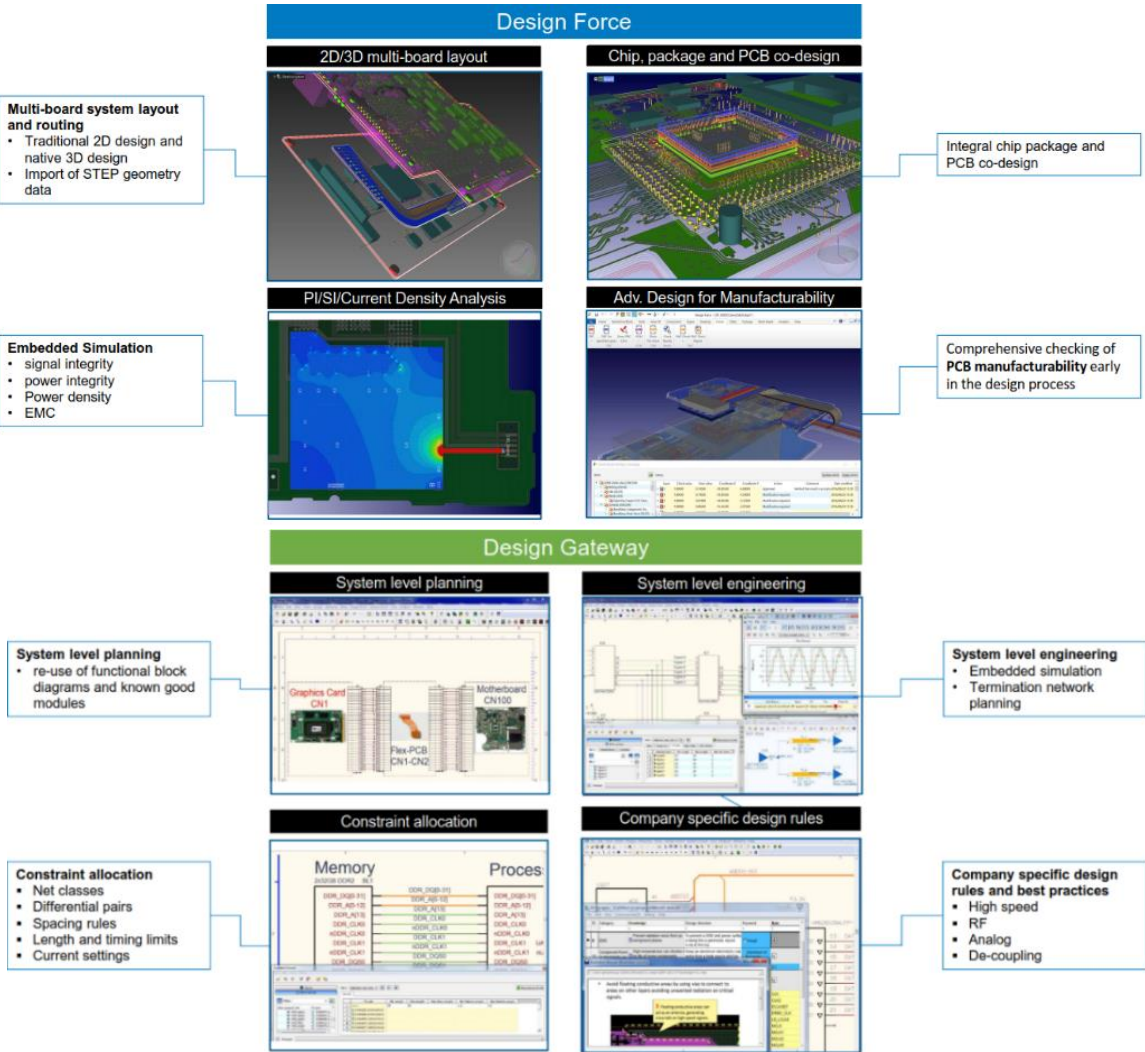
5.4.1.1 The requirements of Clause 5 shall apply to each item and element developed according to ISO 26262, except for off-the-shelf hardware parts, if either of the following applies:

- a) there are no specific hardware safety requirements allocated to the hardware parts, or
- b) the off-the-shelf hardware parts are qualified according to well-established procedures based on worldwide quality standards (e.g. AEC standards for electronic components), and the qualification of the parts is in regard to the intended application.

Component selection according to AEC standard



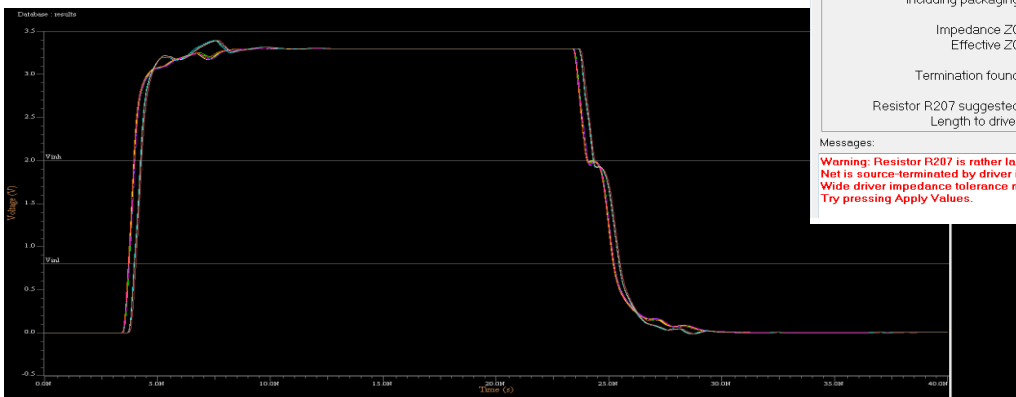
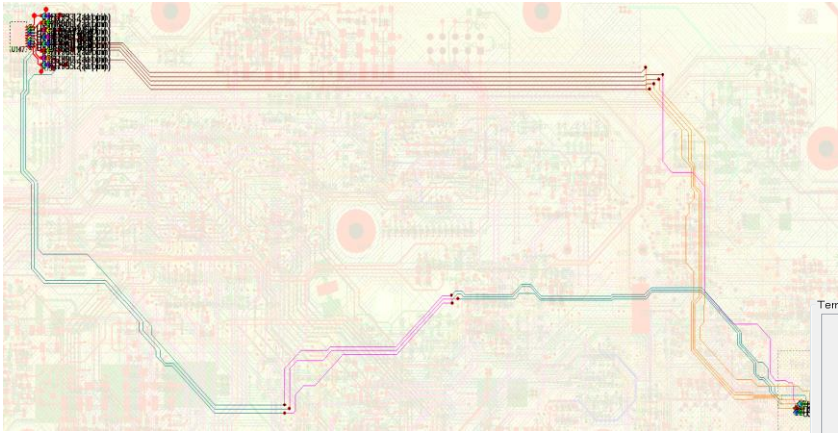
Multi-board design.
 Conventional 2D and 3D design.
 Design to manufacturing checks.



HARDWARE DESIGN

DRAWING & ROUTING

Signal and power integrity simulation.



Terminator analysis

Net name:	SPI2_CLK
Number driver ICs:	1
Number receiver ICs:	1
Number resistors:	1
Number capacitors:	0
Driver impedance:	41.4 ohms
Driver transition time:	0.679 ns
Total net length, not including packaging:	21.258 cm
Impedance Z0:	46.4 ohms
Effective Z0:	42.2 ohms
Termination found:	series termination
Resistor R207 suggested:	0.7 ohms
Length to driver:	0.334 cm

Messages:

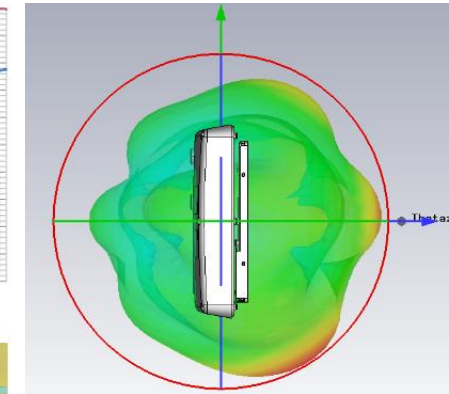
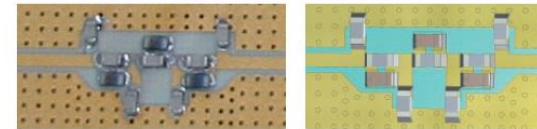
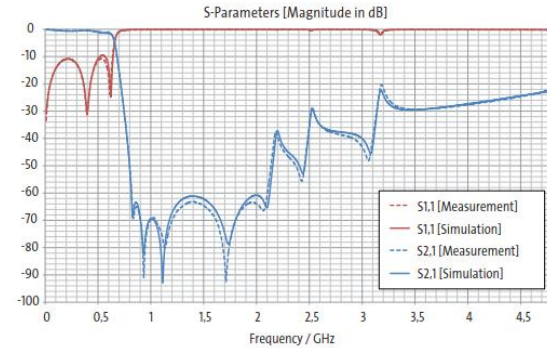
Warning: Resistor R207 is rather large (found 33.0 ohms). Net is source-terminated by driver impedance. Wide driver impedance tolerance may hinder termination. Try pressing Apply Values.



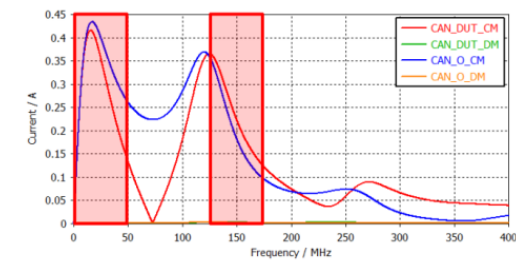
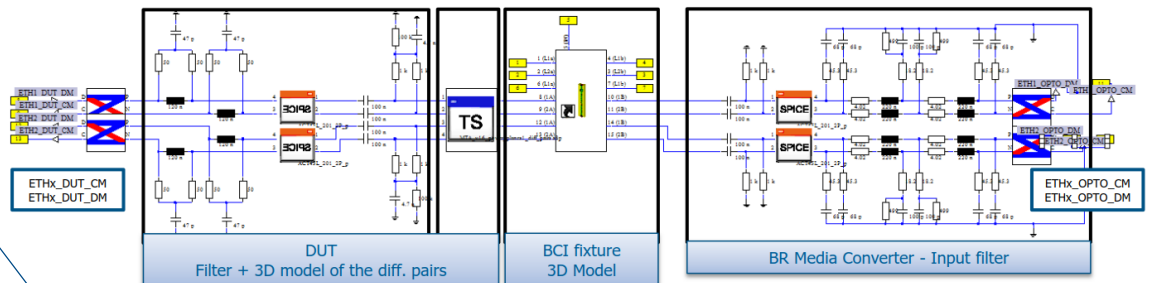
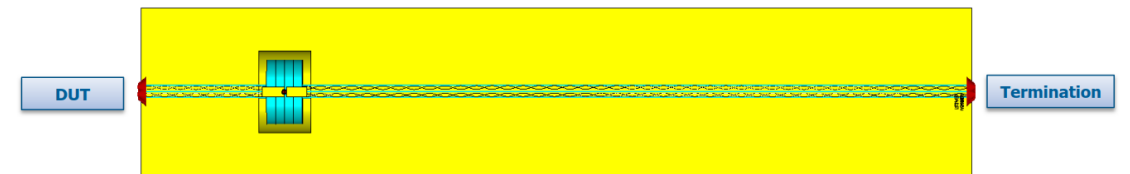
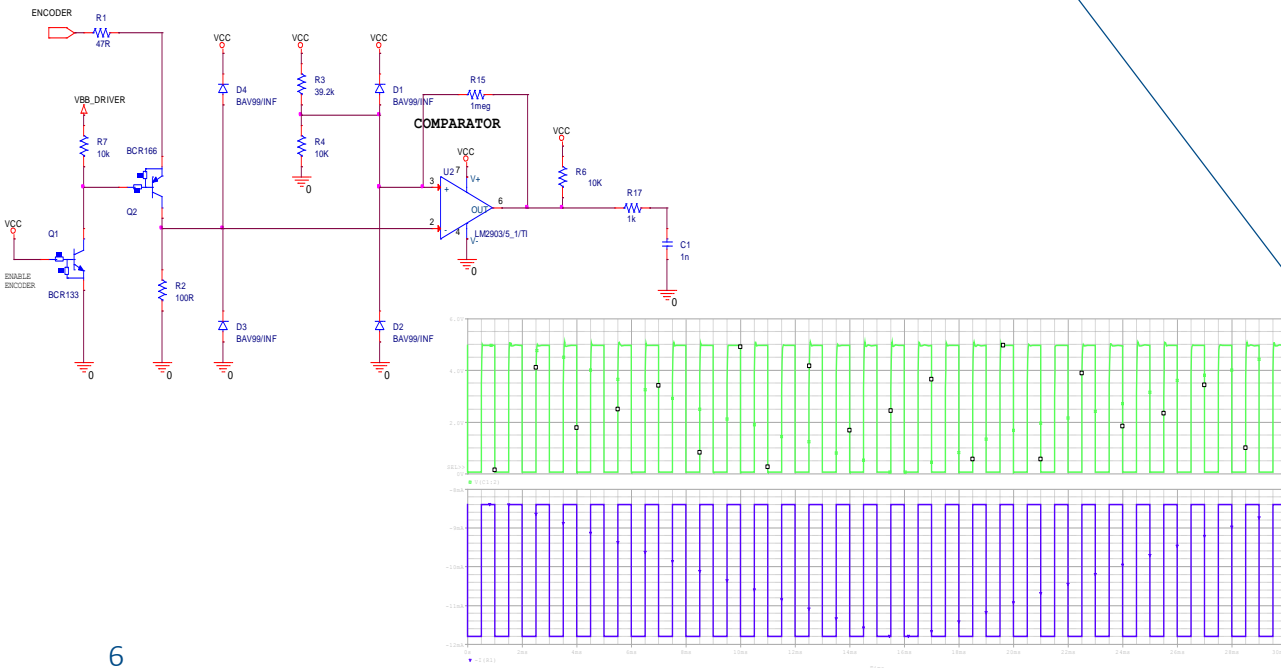
HARDWARE DESIGN

ELECTROMAGNETIC SIMULATION

Tools for simulating and optimizing electromagnetic systems for EMC on printed circuit boards (PCB).



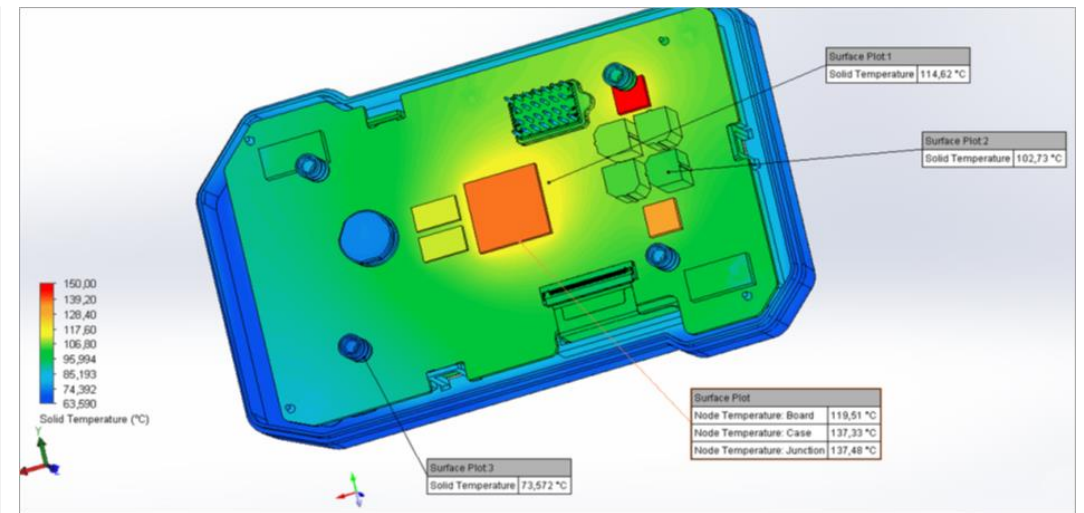
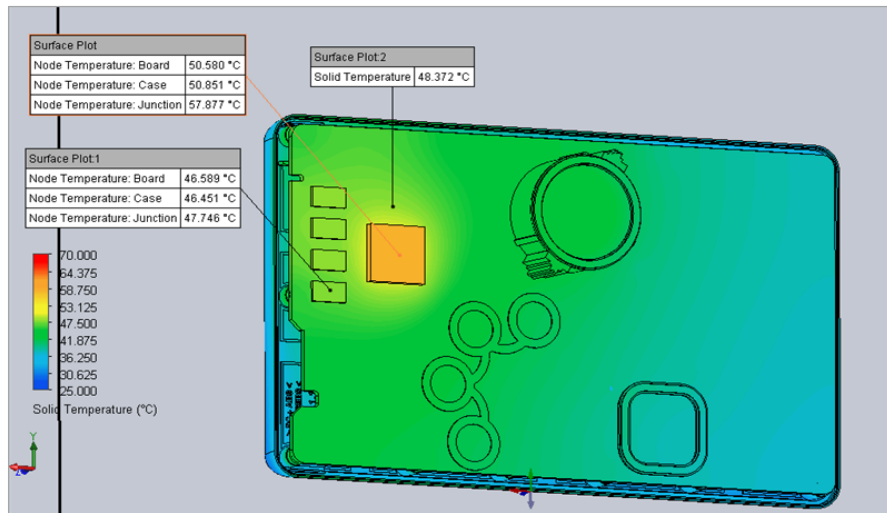
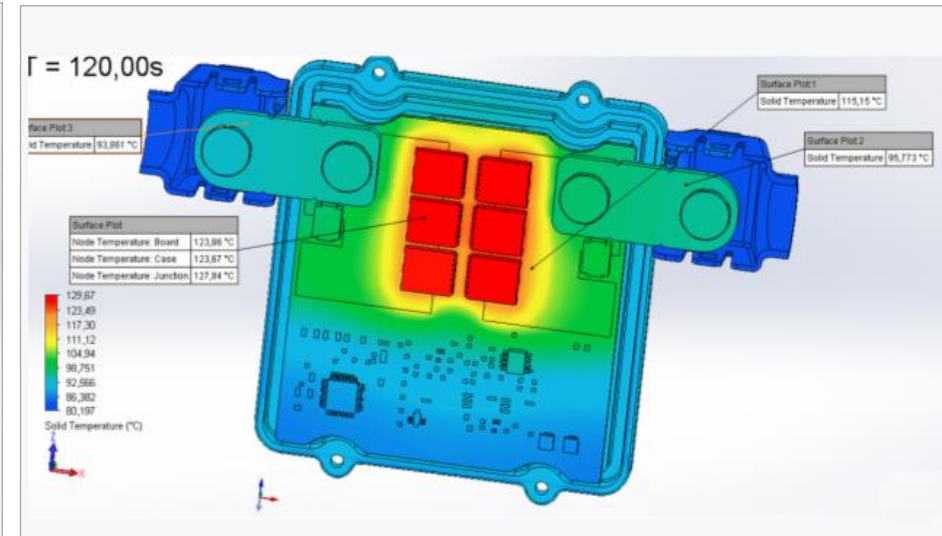
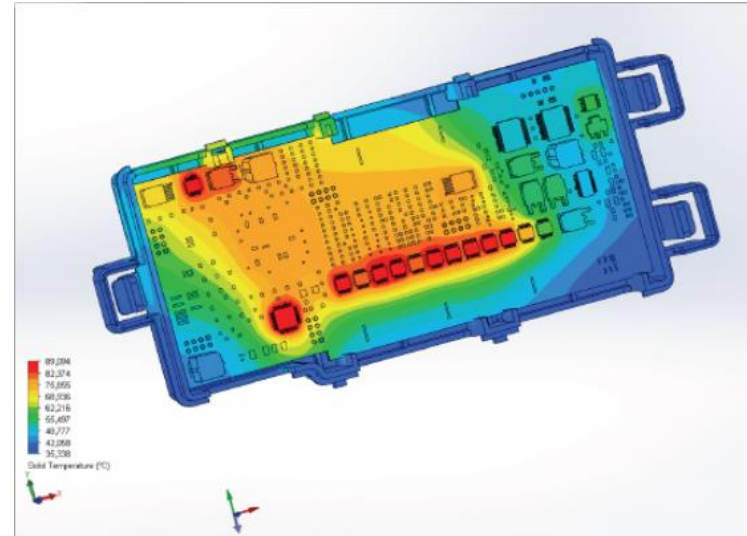
ELECTRICAL SIMULATION



HARDWARE DESIGN

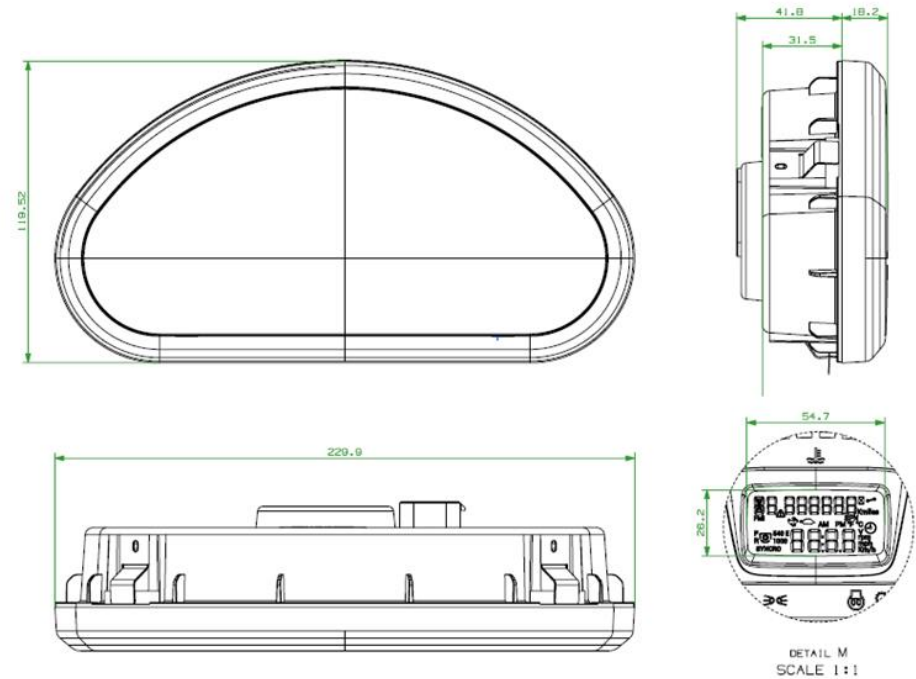
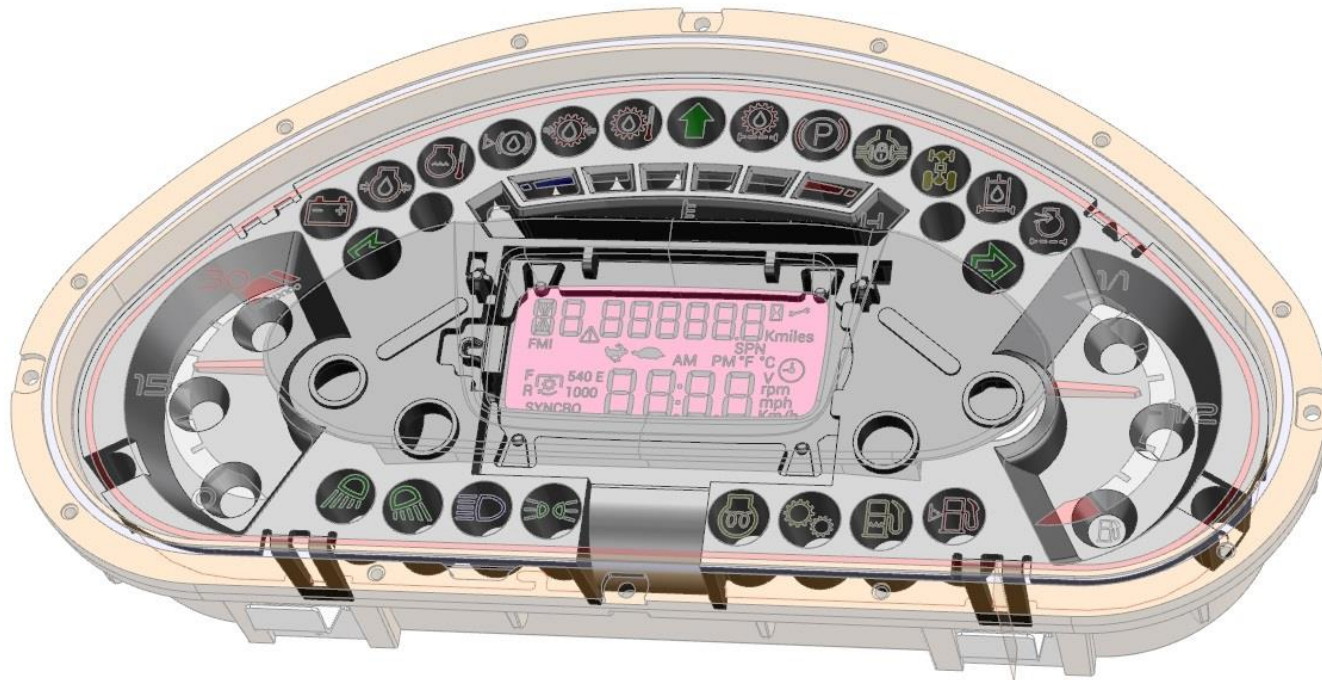
CFD THERMAL DESIGN AND ANALYSIS

Thermal simulation tool.



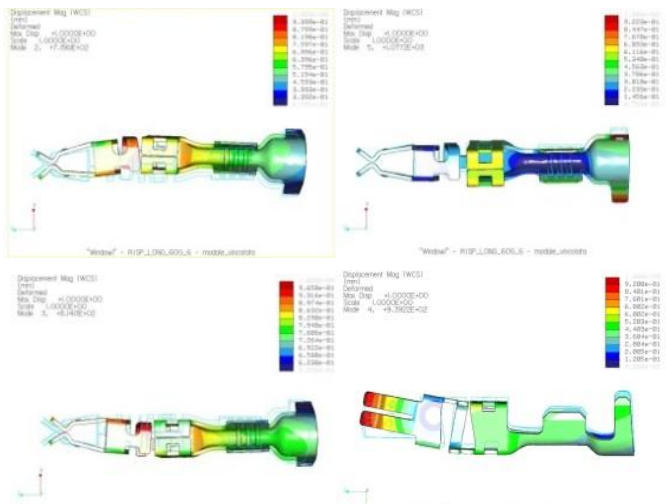
MECHANICAL DESIGN

DESIGN PROCESS – 3D Modelling Tools
and CAE Tools



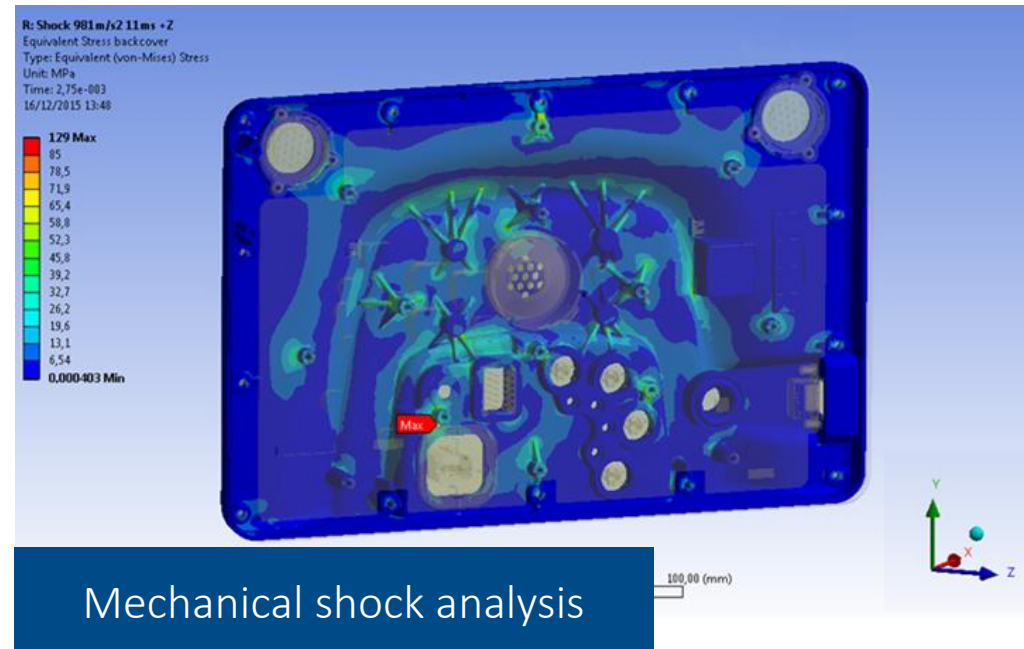
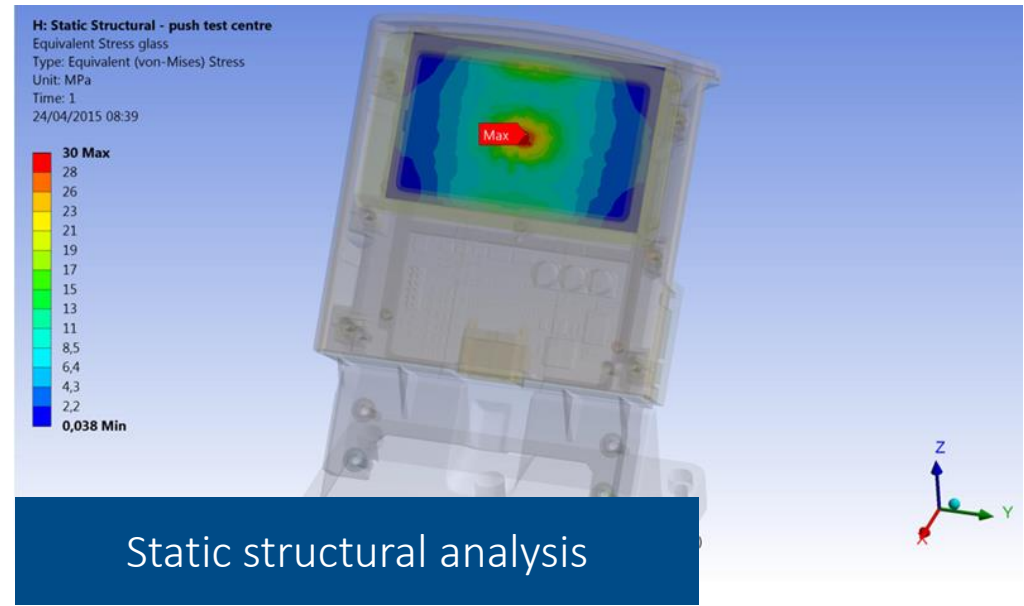
MECHANICAL DESIGN

CAE ANALISYS



Mode	Frequency [Hz]
1	106,03
2	158,31
3	200,83
4	243,89
5	279,22

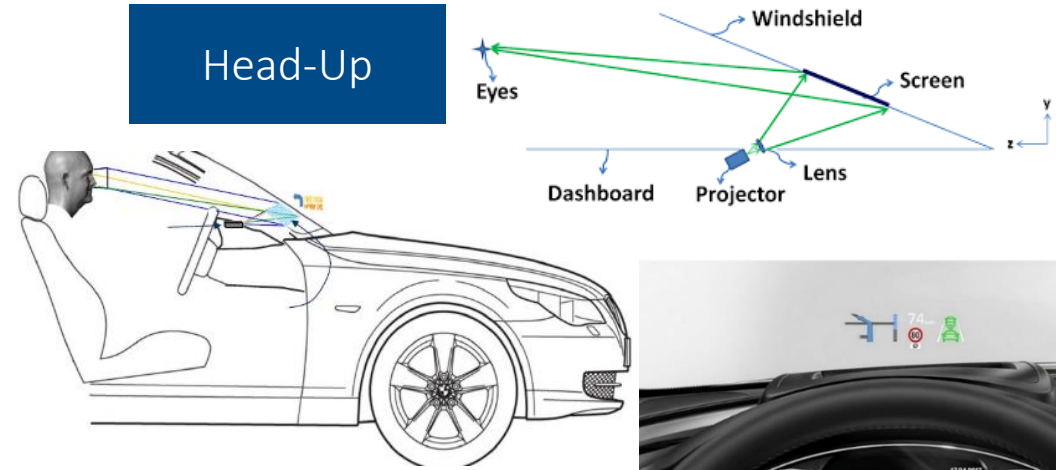
Modal/vibrational (sine or random) analysis



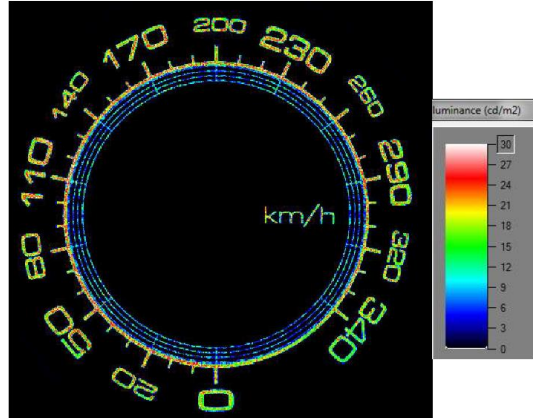
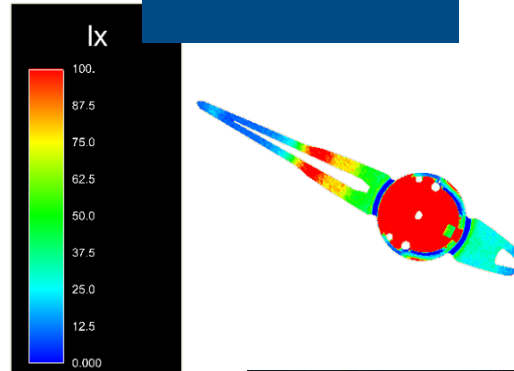
MECHANICAL DESIGN

OPTICAL ANALYSIS – Telltales, Dial plates, Display and Head-Up

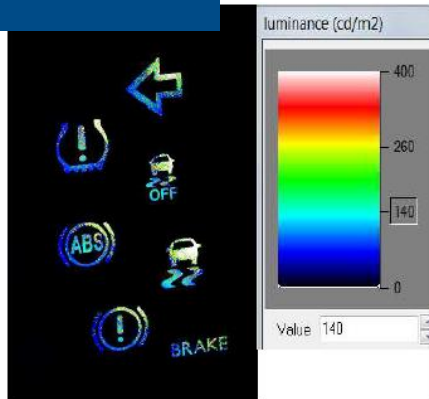
Head-Up



Pointers



Dial plates and telltales



Displays



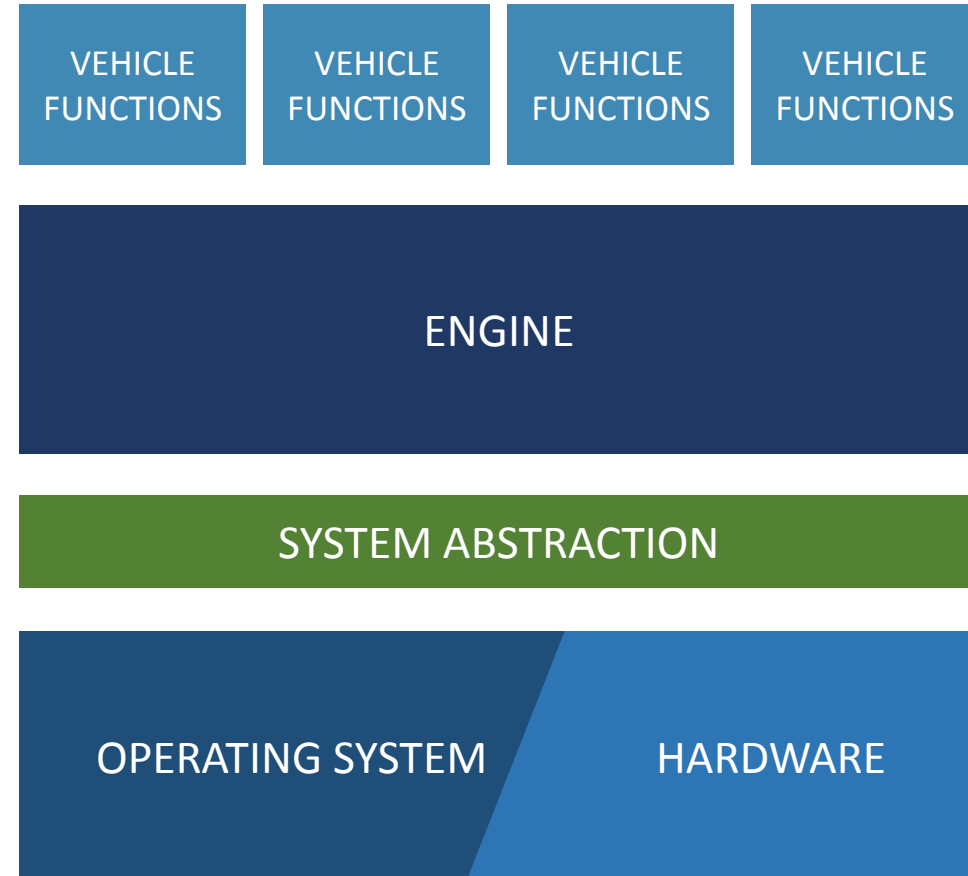
SOFTWARE DESIGN

MTA CORE 

MTA Core is a software package that contains general purpose libraries plus an engine that manages all typical functional states of automotive application.

Main features:

- OS independent
- hardware independent
- layered and modular approach
- small footprint
- optimized
- code reuse oriented



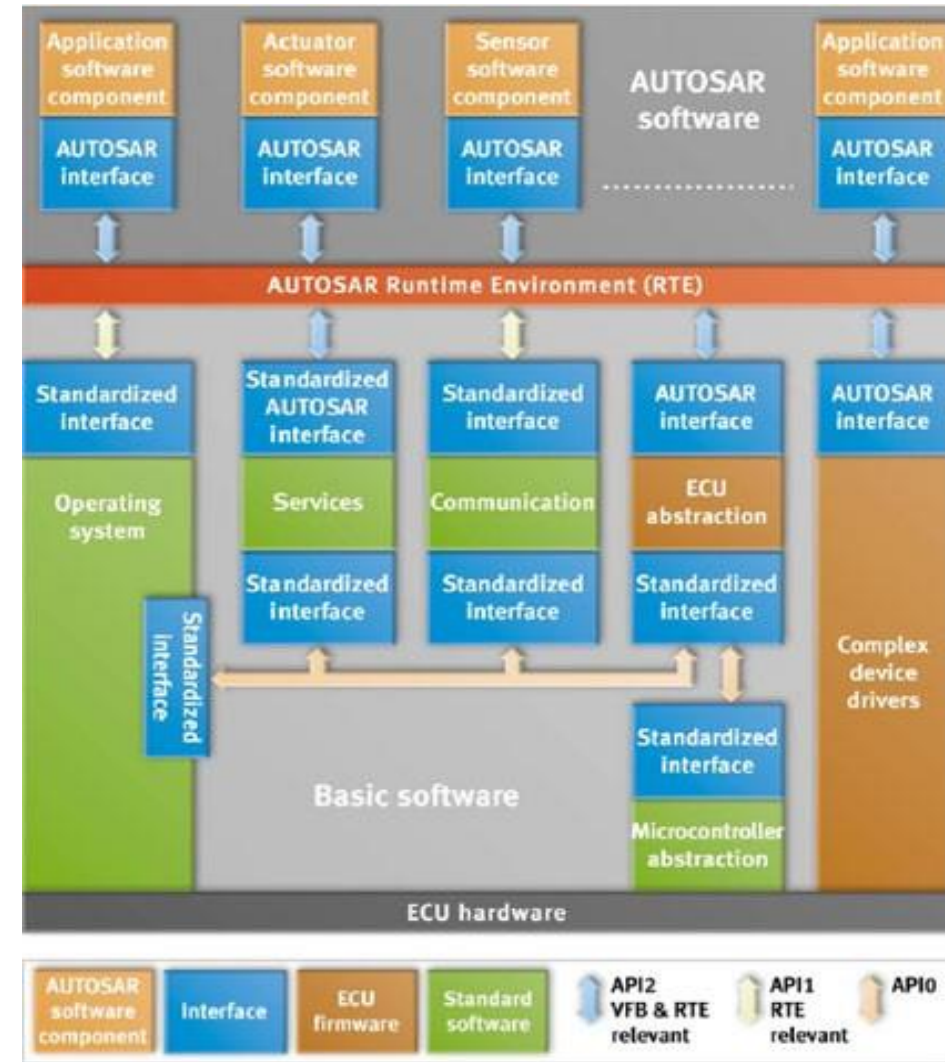
SOFTWARE DESIGN



Since 2010 MTA develops its projects using the Autosar (3.1/4.X) environments which are certifiable at different ASIL levels (ISO 26262/25119):

Examples of complex drivers developments:

- motor DC/DC control
- motor brushless control
- stepper motor control
- power driver
- board protection system
- TFT/LCD displays
- sound generator



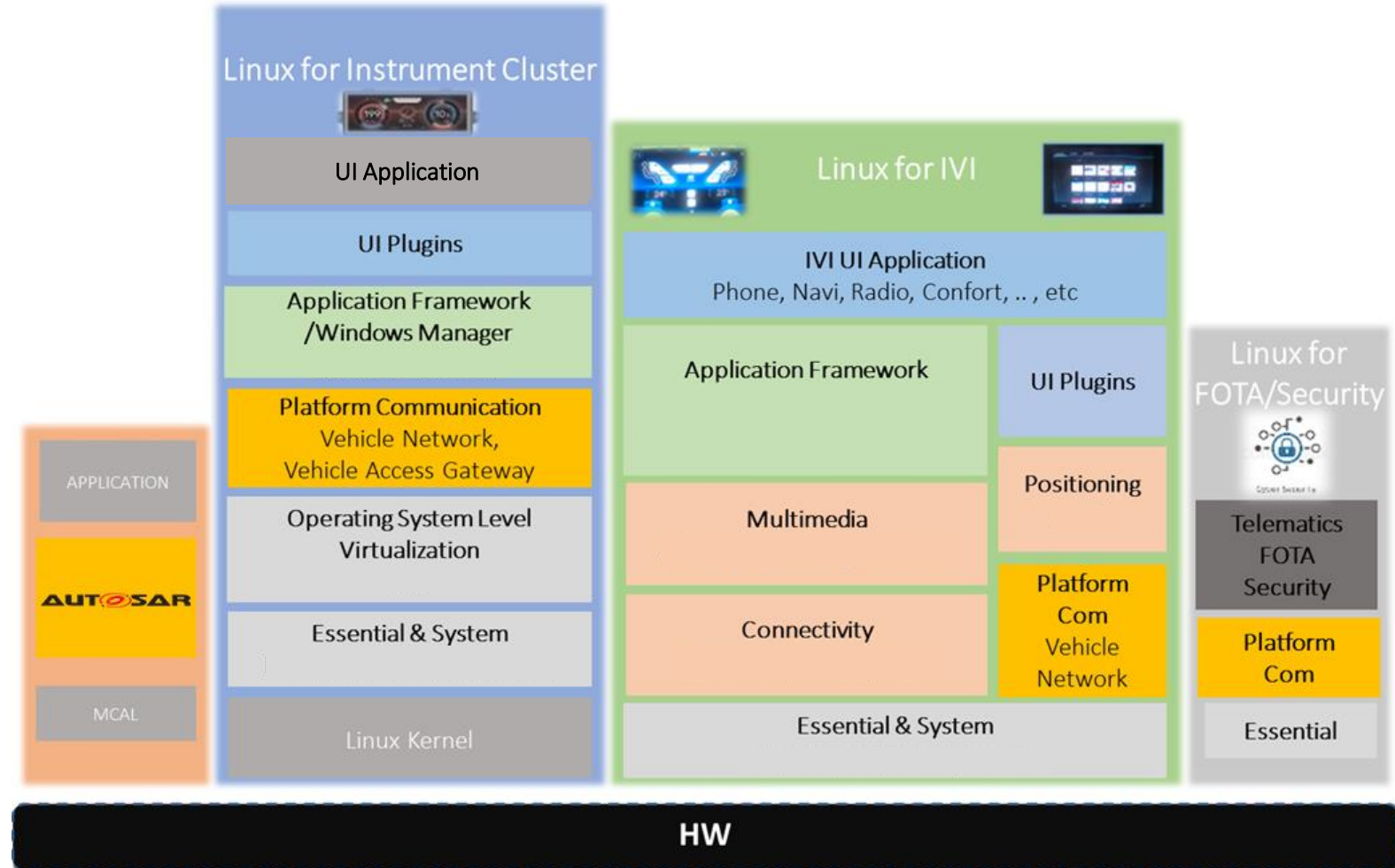
SOFTWARE DESIGN

LINUX



Linux optimization by MTA.

- Linux for instrument cluster
 - Quick Boot in milliseconds
 - 50ms CAN messages
 - graphics in less than 1 sec
 - fast and complex graphics
 - 2.5D, 3D at 60fps
 - multi-display
- Linux for infotainment
 - multimedia (radio, audio/video)
 - connectivity (cellular, BT...)
 - positioning (navigation)
- Linux for security
 - security extensions
 - firmware over the air
 - secure gateway



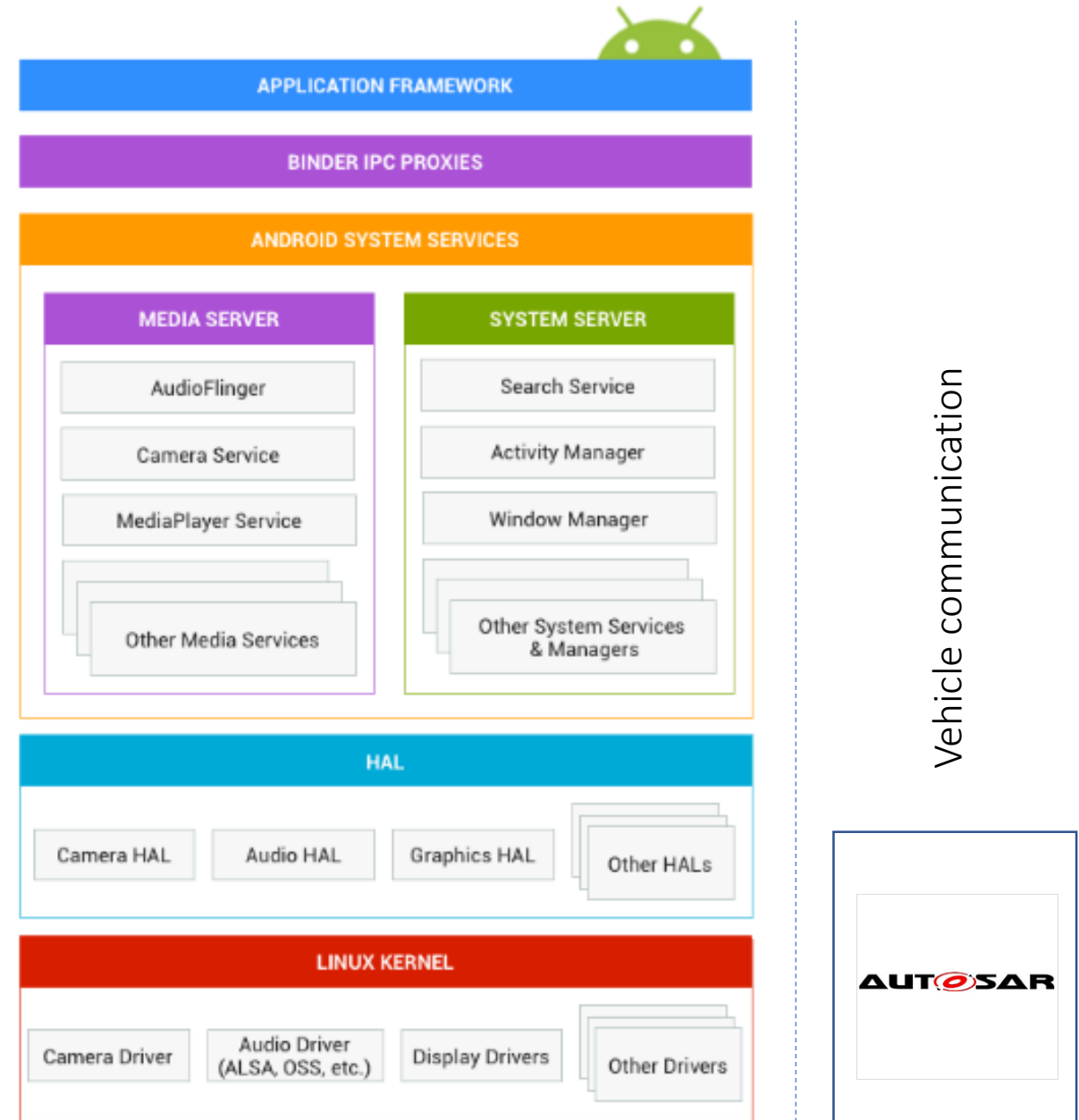
SOFTWARE DESIGN

ANDROID



Android optimization by MTA.

- Android Quick Boot in few seconds (< 4 sec)
- Widely automotive bus support:
 - CAN
 - LIN
 - BroadR-Reach
- Enable advanced functionalities:
 - multimedia
 - connectivity
 - apps
 - complex graphics
- Real-Time:
 - vehicle functionalities implemented in Autosar



AUTOMATION FOR VALIDATION & END-OF-LINE TEST

VISION

Camera image recognition for automatic verification of icons, graphics elements, alarms, messages and telltales.



HARDWARE IN THE LOOP

- Electrical test
- Input/output stress test
- Optical, audio, haptic tests (robots)
- Acquisition systems



THANK YOU

for your attention

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